FIG. 1

FIG. 2
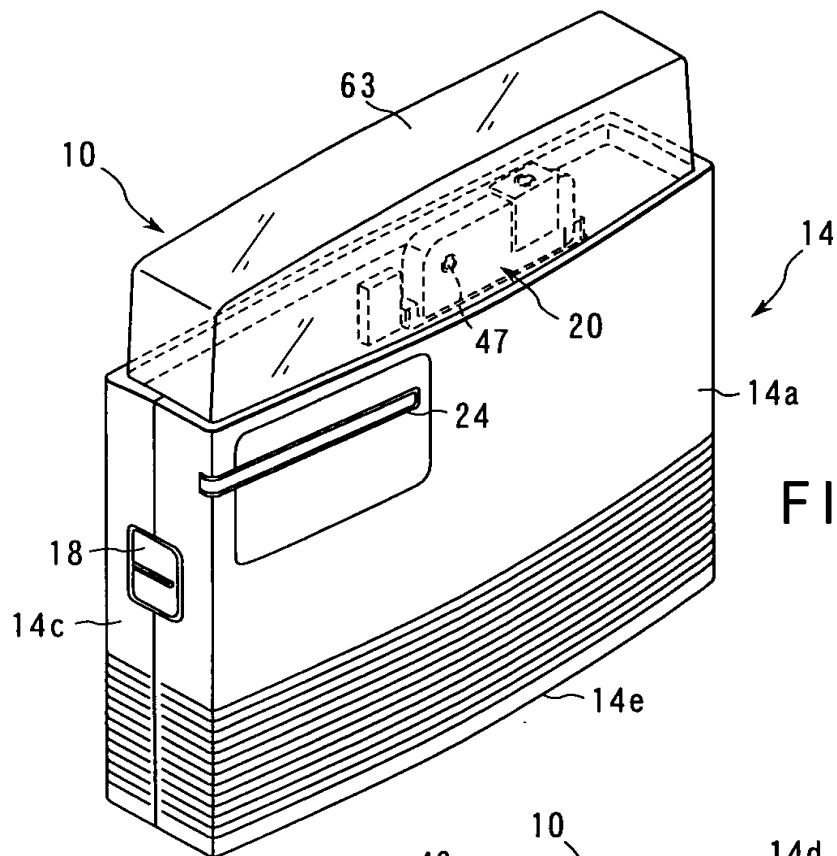
FIG. 3
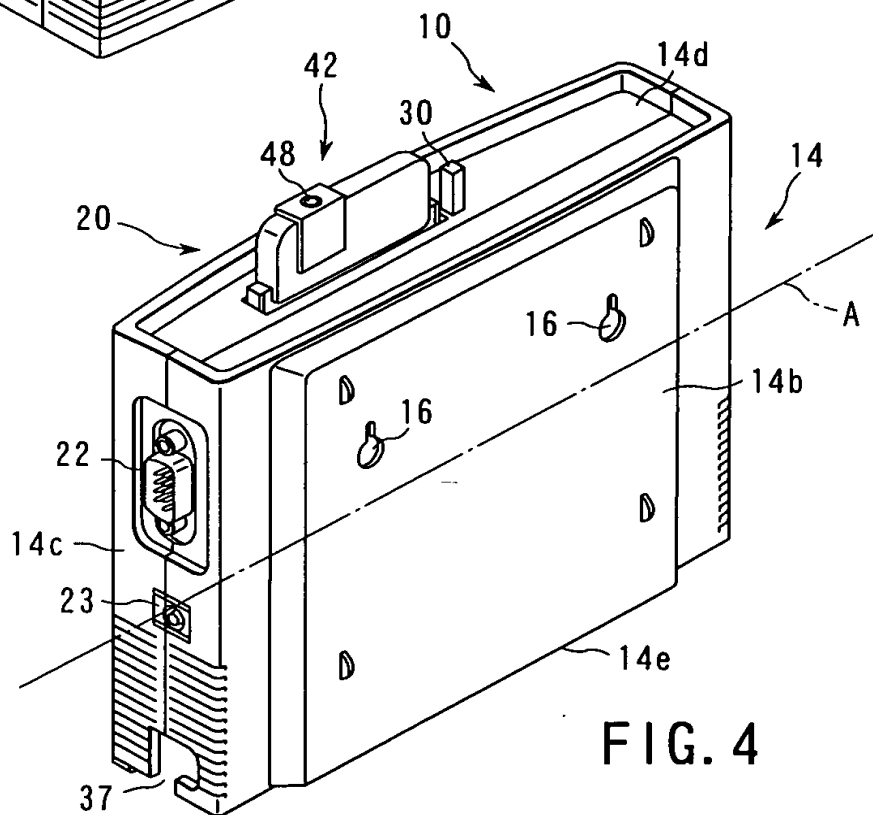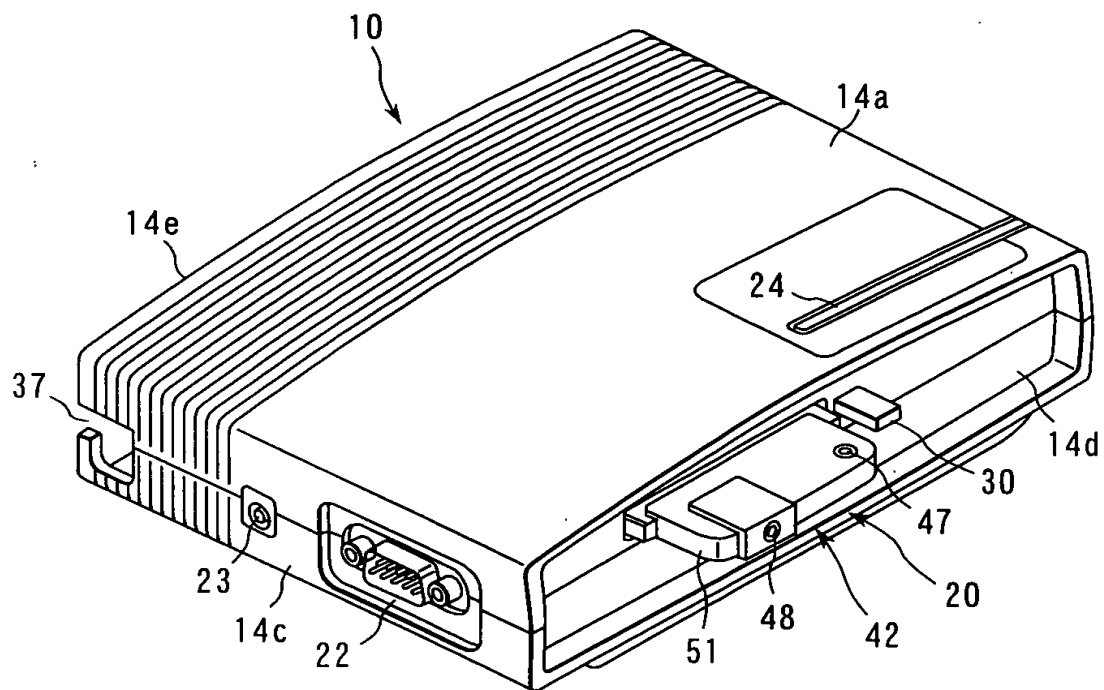

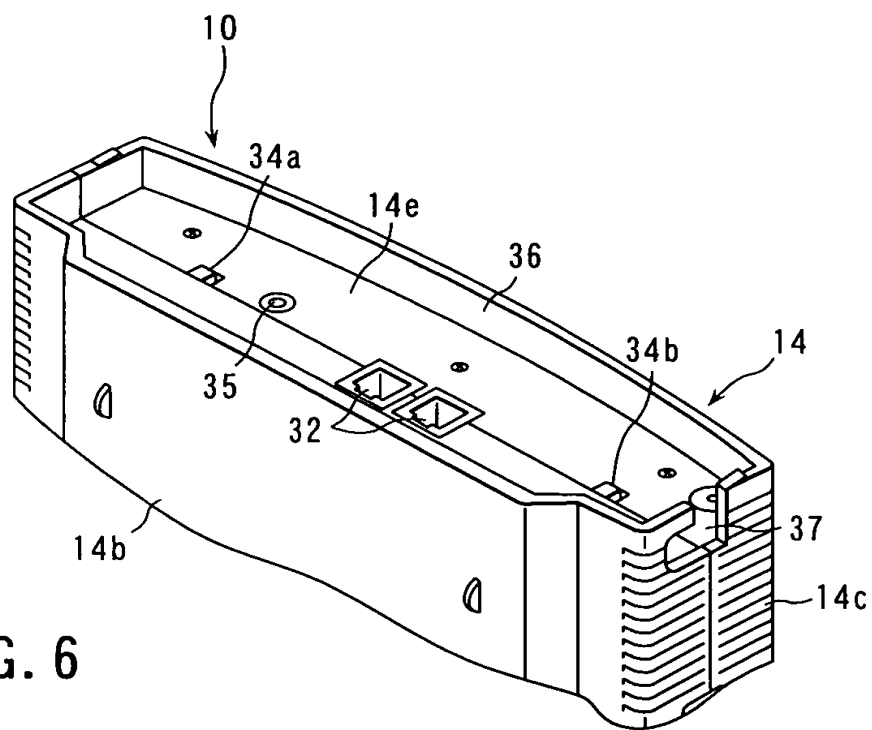FIG. 4

FIG. 5

FIG. 6

FIG. 7
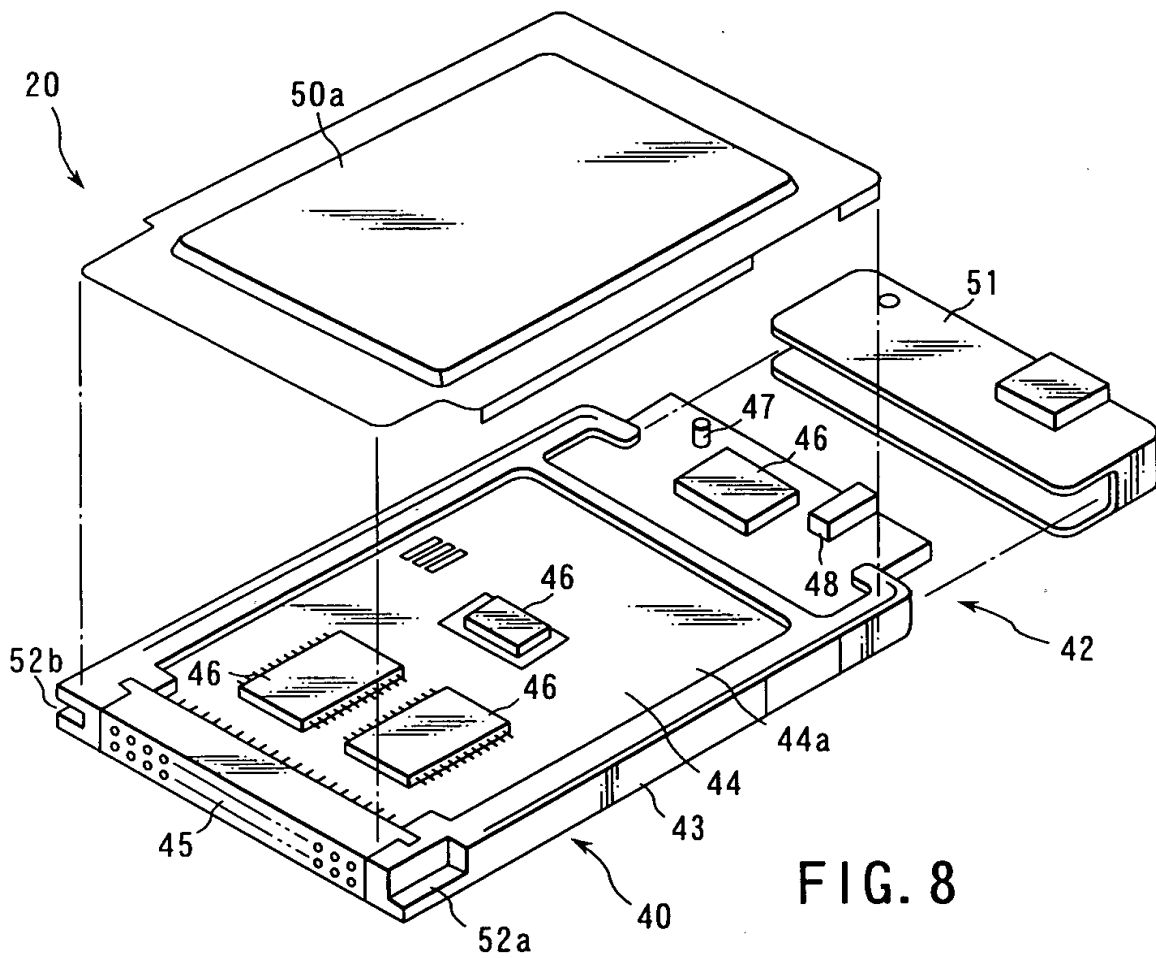
FIG. 8
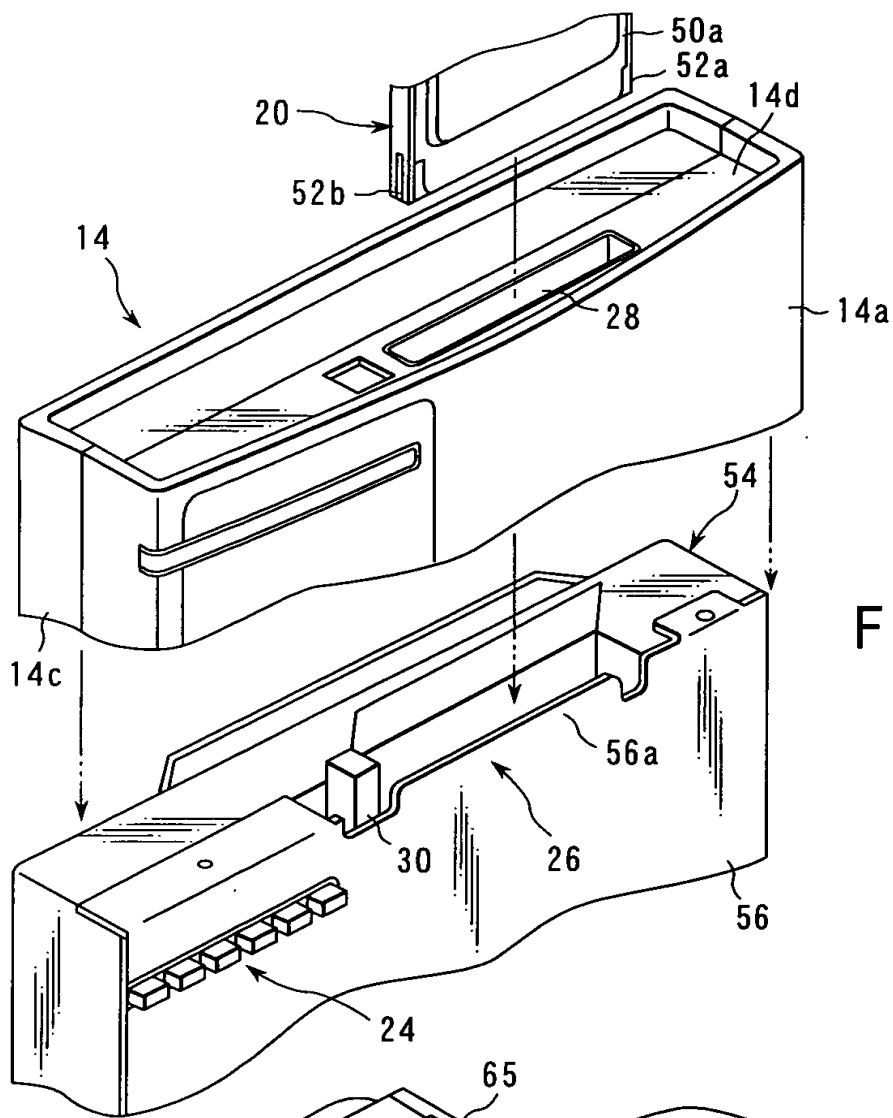
FIG. 9
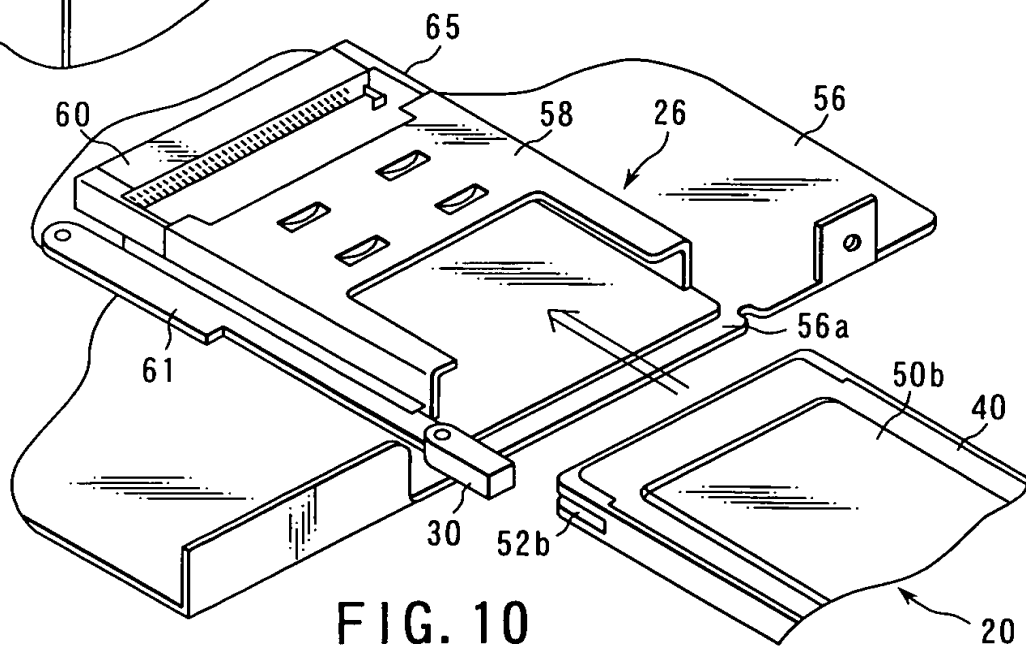
FIG. 10
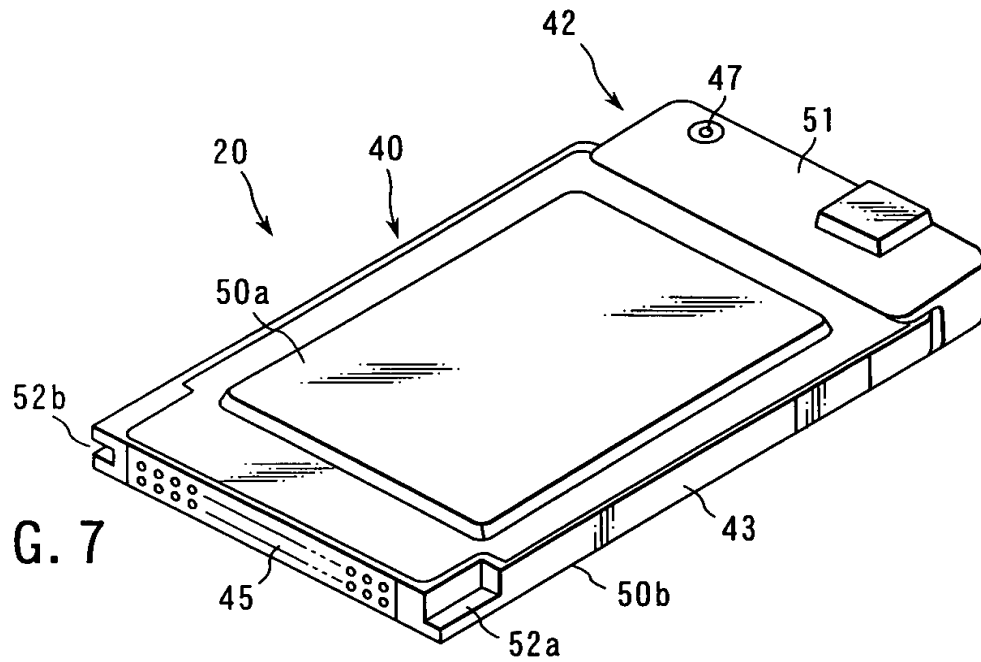
FIG. 11

FIG. 12

FIG. 14

FIG. 13

CRYSTAL OSCILLATOR ELEMENT — 83

LED — 47

HEADSET PORTION — 48

81

84

20

RF ELEMENT

BASE BAND ELEMENT

AD/DA

80

ANTENNA ELEMENT

82

MEMORY

46

I/F

45

10

I/F — 60

CPU — 72

POWER SUPPLY ELEMENT — 77

AC ADAPTER TERMINAL — 23

24

LED

MAIN MEMORY — 74

ROM — 76

CHANGEOVER SWITCH — 78

RS232C CONNECTOR — 22

MODEM ELEMENT — 70

SWITCH

MODULAR JACK — 32

34a, 34b, 35

12

PUBLIC DATA NETWORK
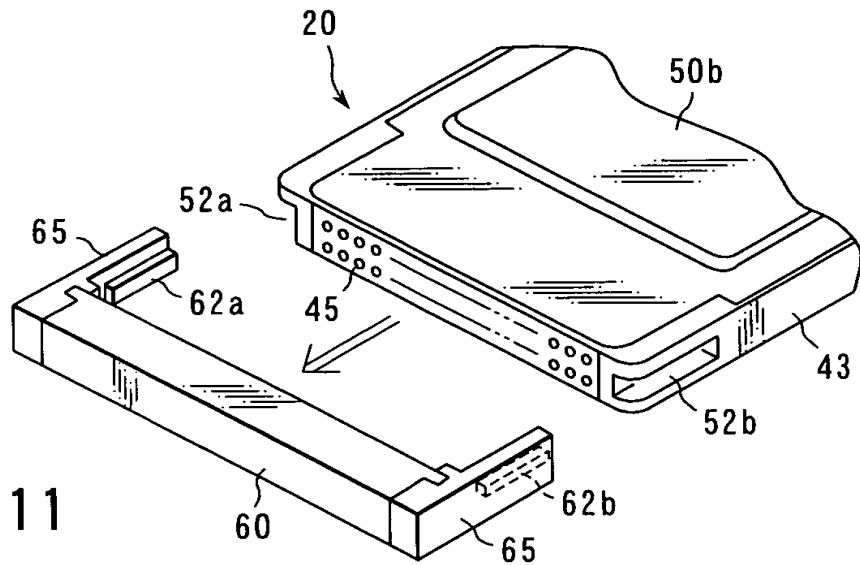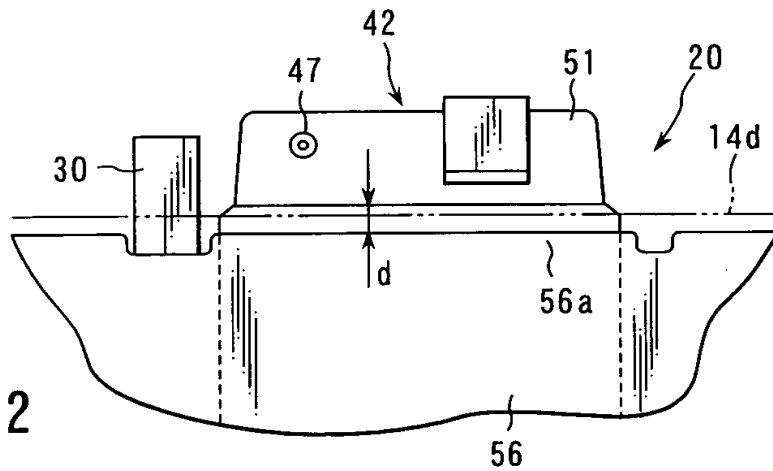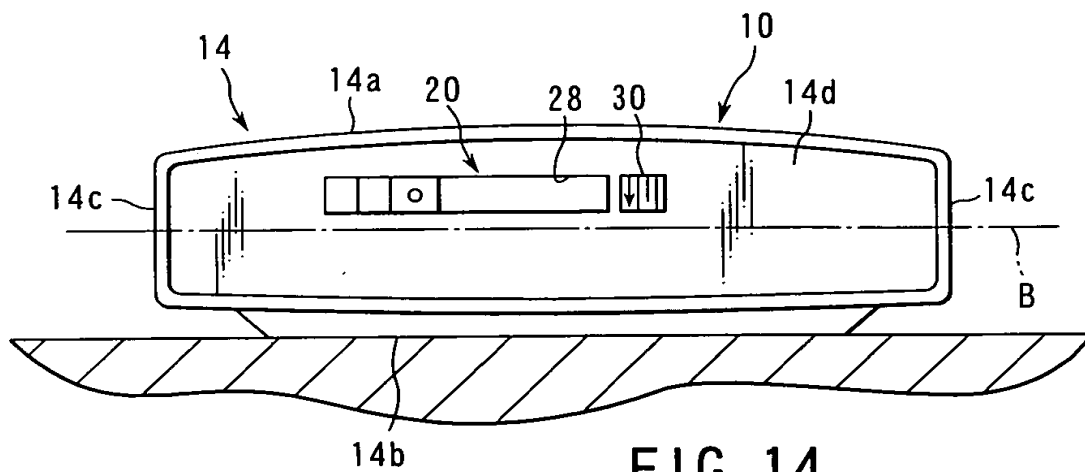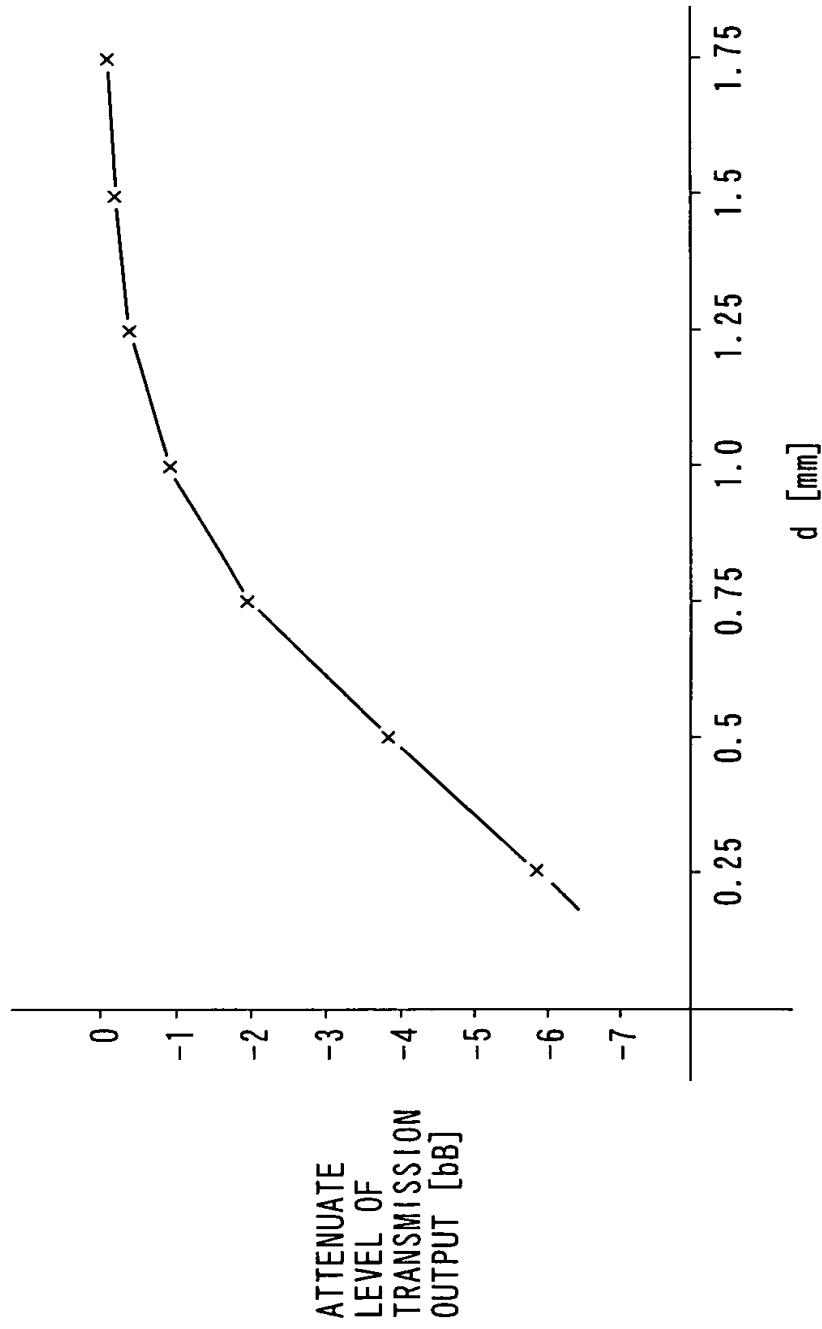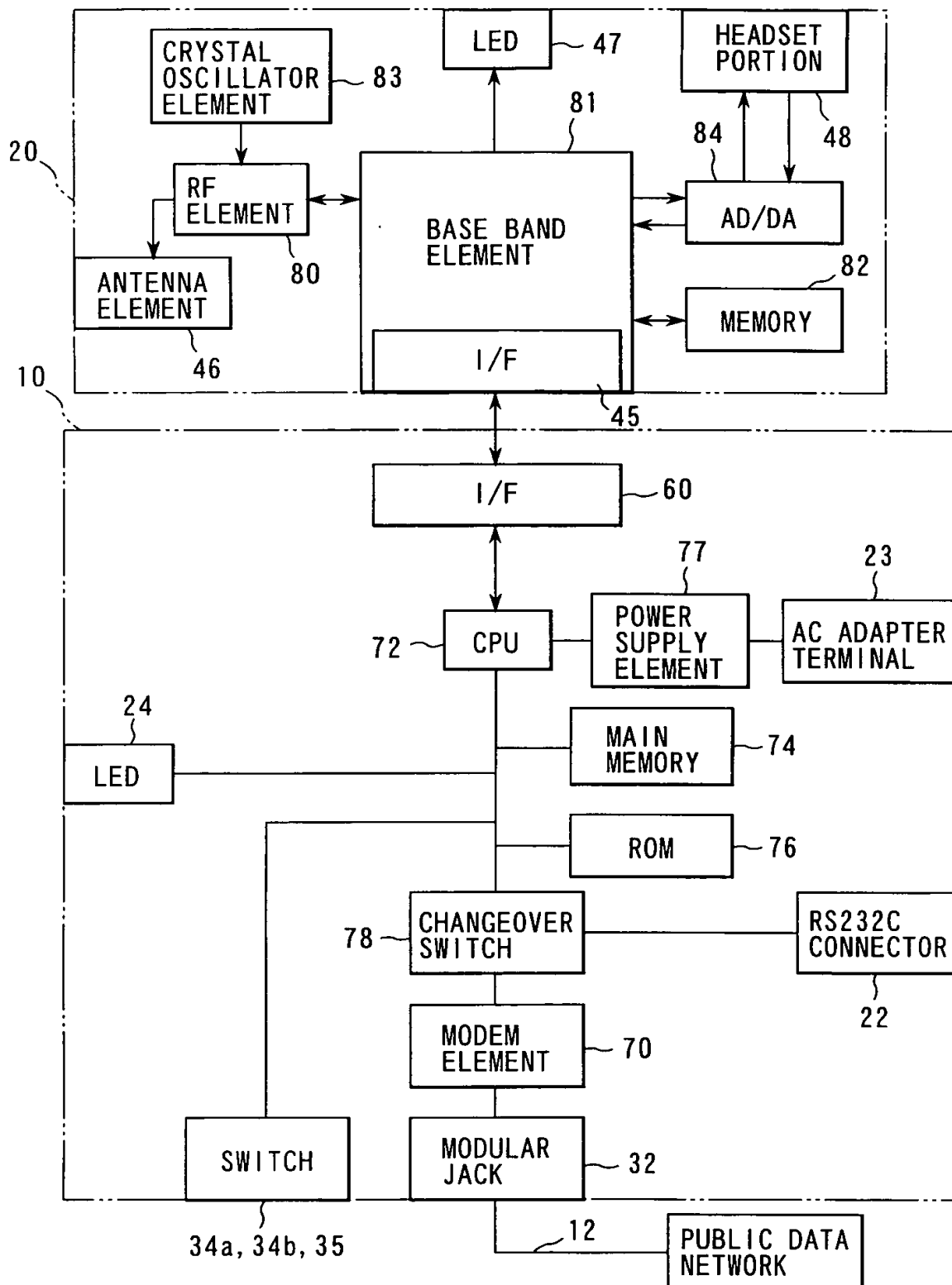
FIG. 15
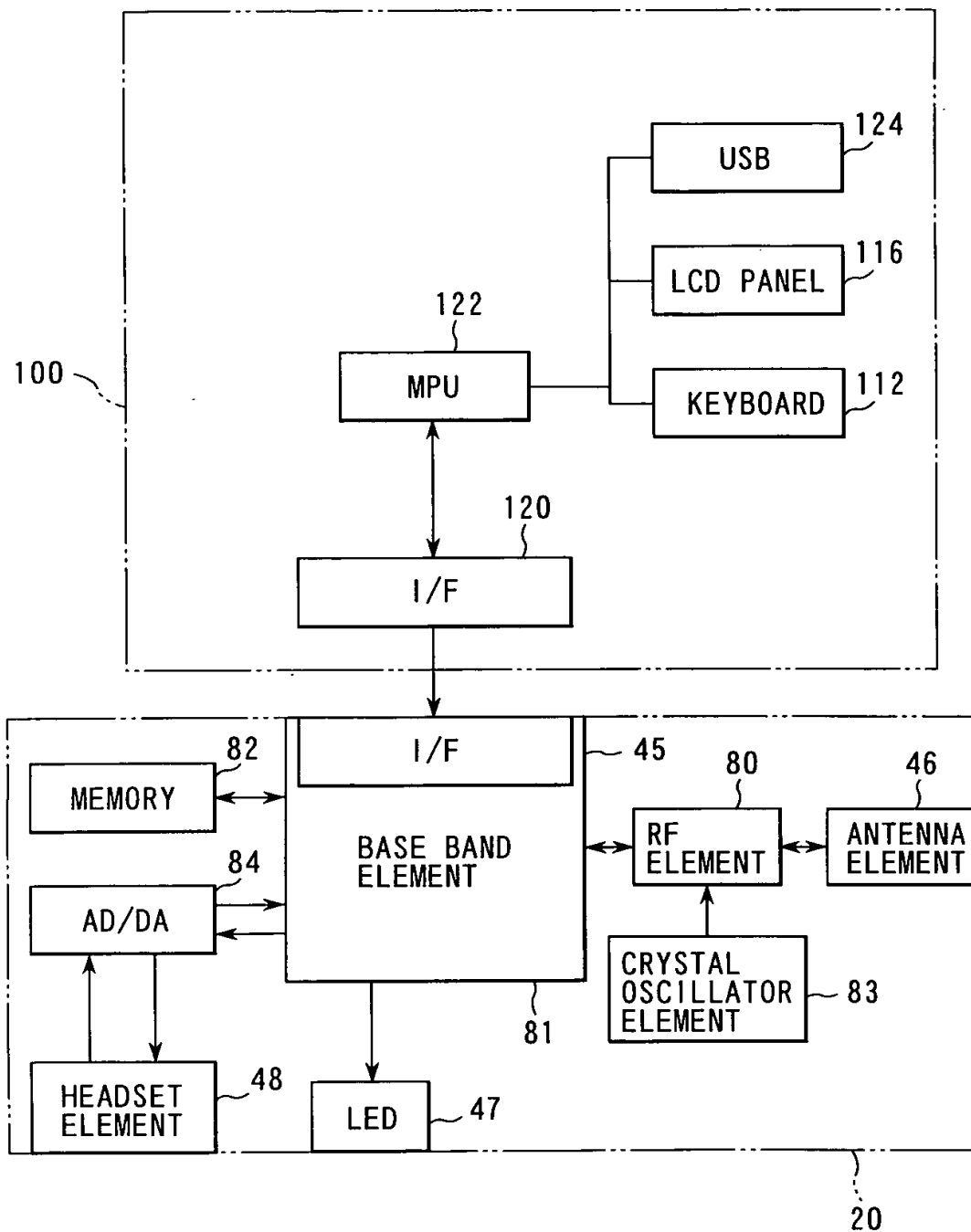
F I G. 16

SETTING OF SLIDE & ROTARY SWITCHES

	OPERATING STATES	SLIDE SW1	SLIDE SW2	ROTARY SWITCH POs. #	1	2	3	4	REMARKS
USER SETTING	NORMAL (MODEM)	L	L	0,2,4,6	L	x	x	L	
	NORMAL (AT)	H	L	0,2,4,6	L	x	x	L	
	MAINTENANCE (MODEM)	L	H	0,2,4,6	L	x	x	L	CPU PROHIBITS INTERRUPTION OF UART SYSTEM
	MAINTENANCE (CPU)	H	H	0,2,4,6	L	x	x	L	PIN CODE CHANGED
SETTING FOR MAINTENANCE OPERATION	INITIALIZATION 1	L	L	1,3,5,7	H	x	x	L	EEPROM INITIALIZATION EXECUTED
	INITIALIZATION 0	H	L	1,3,5,7	H	x	x	L	EEPROM INITIALIZATION STARTED
	RESERVED	L	H	1,3,5,7	H	x	x	L	RESERVED
	TEST MODE	H	H	1,3,5,7	H	x	x	L	PCB INSPECTION, CPU F/W RELOADING, ETC.
SETTING FOR SHIPMENT	RESERVED	L	L	8	L	x	x	H	
	TCI (HCI)	H	L	8	L	x	x	H	FOR HCI EVALUATION
	RESERVED	L	H	8	L	x	x	H	
	TCI (L2CAP)	H	H	8	L	x	x	H	L2CAP EVALUATION
	RESERVED	L	L	9	H	x	x	H	MODEM-DIR STATE
	RESERVED	H	L	9	H	x	x	H	TA STATE
	RESERVED	L	H	9	H	x	x	H	MODE STATE
	RESERVED	H	H	9	H	x	x	H	TA STATE

FIG.17